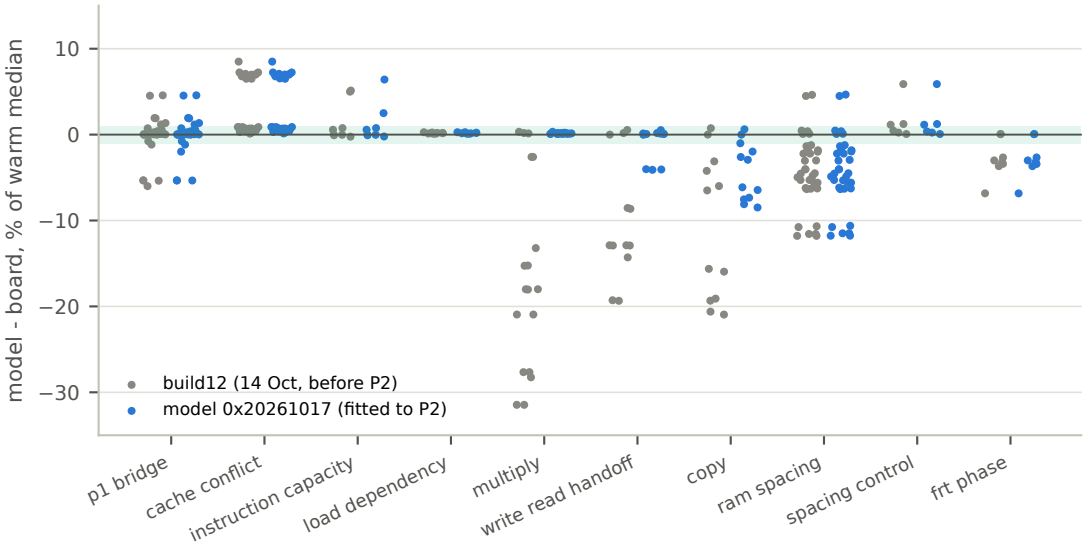

model - board, % of warm median
10
0
−10
−20
−30
build12 (14 Oct, before P2)
model 0x20261017 (fitted to P2)
p1 bridge
cache conflict
instruction capacity
load dependency
multiply
write read handoff
copy
ram spacing
spacing control
frt phase