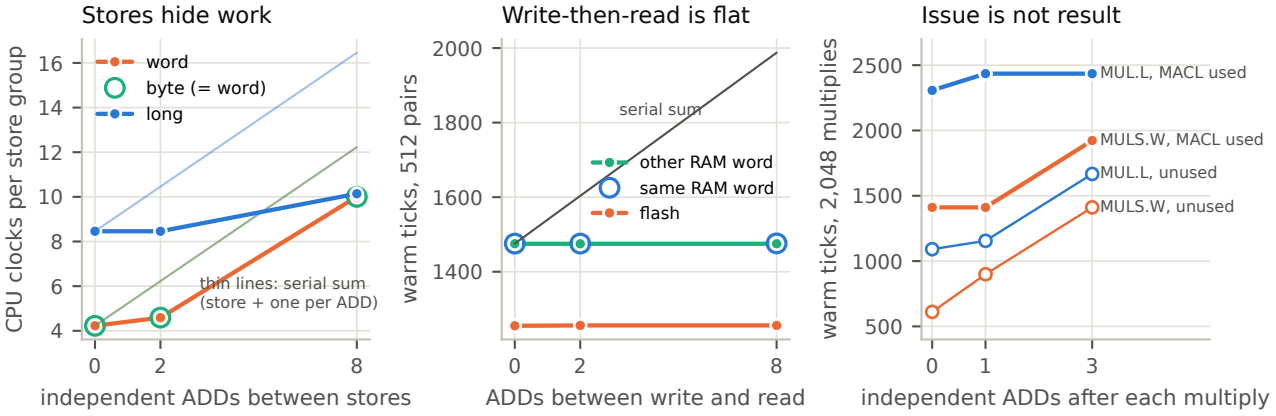

Stores hide work
CPU clocks per store group
16
14
12
10
8
6
4
word
byte (= word)
long
thin lines: serial sum
(store + one per ADD)
0
2
8
independent ADDs between stores
Write-then-read is flat
warm ticks, 512 pairs
2000
1800
1600
1400
serial sum
other RAM word
same RAM word
flash
0
2
8
ADDs between write and read
Issue is not result
warm ticks, 2,048 multiplies
2500
2000
1500
1000
500
MUL.L, MACL used
MULS.W, MACL used
MUL.L, unused
MULS.W, unused
0
1
3
independent ADDs after each multiply