YM2151 Circuit

DIGITAL

YM2151 (U3)
VSS1, IRQ, IC, A0, WR, RD, CS, CT2, CT1, D0, VSS2, D1, SYSCLK, DACCLK, VDD1, SO, SH1, SH2, D7, D6, D5, D4, D3, D2
OPM_CLK, OPM_IRQ, OPM_RST, OPM_A0, OPM_WR, OPM_RD, OPM_CS, OPM_CT2, OPM_CT1, OPM_DB[0], OPM_DB[1], OPM_DB[7:0]

YM3012 (U5)
VDD1, CLK, DGND, DATA, SH2, SH1, IC, AGND1, AGND2, BIASp, BIASn, Vdiv2, TOBUF, COM, CH2, CH1
RBIAS, BC, MP, TOBUF, COM, OPM_ROUT, OPM_LOUT

NJM2043M (U8) — RBIAS, BC, MP, TOBUF, fbCOM
AVDD, VCC

8.000MHz (X1) F8M — V3P3
7.9872MHz (X2) F7M — V3P3
3.579545MHz (X3) F3P5 — V3P3

030412 : R22/R24 = 100K
030412 : R23/R25 = 10K
R32/R33 = 180K - 220K

NJM4558D (U10) — MIX_ROUT, MIX_LOUT — Vo=8/3

PC-9801-86 set : 1.8K - 2.2K
X68000 set : 5.6K - 7.5K

YMF288 Circuit

YMF288 (U4)
VSS1, D0, D1, D2, D3, D4, D5, D6, D7, TEST1, IC, 5V/3V, TEST2, VSS2, MCLK, VDD1, LRO, WCO, DO, BCO, STBY, A1, A0, RD, WR, CS, COM, IRQ
OPNA_LRO, OPNA_CLK, OPNA_D0, OPNA_D1, OPNA_D2, OPNA_D3, OPNA_D4, OPNA_D5, OPNA_D6, OPNA_D7, OPNA_RST, OPNA_A1, OPNA_A0, OPNA_RD, OPNA_WR, OPNA_CS, OPNA_IRQ

YAC513 (U6)
VDD1, VSS1, VDD2, SEL, CLK, WDCK, LRCK, DIN, AOUT, CV, MP, SWIN, TST2, CH2, CH1, TST1
OPNA_AOUT, OPNA_CV, OPNA_MP, OPNA_SWIN, OPNA_ROUT, OPNA_LOUT

NJM2043M (U7)

Mar,3rd,2002
Need this CAPACITOR!!

ANALOG

LEFT
RIGHT
1:10 GAIN
CDOUT, LEFT, GND, GND, RIGHT, CN5
VPOS, VNEG
MARUSHIN MR-101B, CN4

020801 : R34/R36
020802 : R54/R55

3/4

- Neo-Electronics Genesis - KURUSUGAWA Electronics Inc,. [MASATIKU KoBo.]	Designer: Masami IKURA
ITRON / LINUX / Human68K OS SH4 / M68K / PowerPC Architecture ALTERA/XILINX FPGA Design	Check: RMP_V1P5 .sch Rev.
	Date: 2003/04/23 0:02:56
	Name: RMP_V1P5 Sheet 3 of 4